FSMD-A MS

Mechanical samples of the FSMD-A package with no die

Description

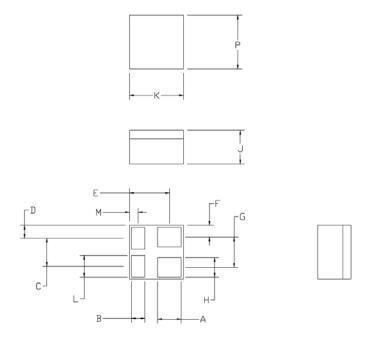
Freebird Semiconductor FSMD-A package has been designed for use with GaN technology.

These parts are supplied with no die inside.

I/O Pin Assignment (Bottom View)

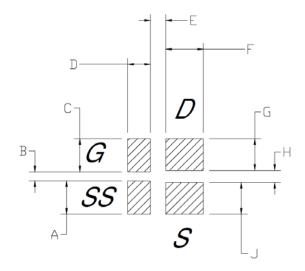
Pin	Symbol	Description	Visual Outline	Physical device
1	G	Gate		
2	D	Drain	1 3	
3	SS	Source Sense	2 4	
4	S	Source		and along

PACKAGE OUTLINE AND DIMENSIONS



	Inches		Millimeters		
Symbol	Min	Max	Min	Max	Note
Α	0.052	0.062	1.320	1.574	
В	0.027	0.037	0.685	0.939	
С	0.063	0.073	1.600	1.854	
D	0.026	0.036	0.660	0.914	
Е	0.092	0.102	2.336	2.590	
F	0.024	0.034	0.609	0.863	
G	0.068	0.078	1.727	1.981	
Н	0.042	0.052	1.066	1.320	
J	0.080	0.090	2.032	2.286	Ref.only
K	0.125	0.135	3.175	3.429	
L	0.048	0.058	1.219	1.473	
M	0.016	0.026	0.406	0.664	
Р	0.125	0.135	3.175	3.429	

FSMD-A FOOTPRINT FOR PRINTED CIRCUIT BOARD DESIGN



Symbol	Inches		Millimeters		Note
	Min	Max	Min	Max	Note
Α	0.051	0.061	1.295	1.549	
В	0.010	0.020	0.254	0.508	
С	0.051	0.061	1.295	1.549	
D	0.033	0.043	0.838	1.092	
Ε	0.020	0.030	0.508	0.762	
F	0.058	0.068	1.473	1.727	
G	0.048	0.058	1.219	1.473	
Н	0.015	0.025	0.381	0.635	
J	0.048	0.058	1.219	1.473	

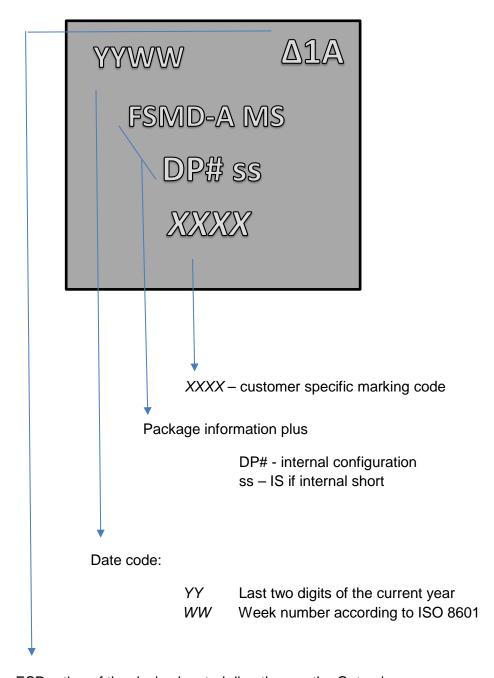
Freebird Semiconductor Part Number Information

PART NUMBERS THAT CAN BE ORDERED	DESCRIPTION	DIAGRAM : bottom view with INTERNAL shorts marked in red	SHIPPING
FSMD-A MS DP#	Standard package with an internal short between S & SS contacts	1 3 2 4	VA/- (() - 1
FSMD-A MS DP# IS	Standard package with an internal short between S & SS and G & D contacts	2 4	Waffle trays

= 2 is relevant for the following products FBG04N08Ax FBG10N05Ax

= 4 is relevant for the following products FBG20N05Ax

Freebird Semiconductor Product Marking Information



ESD rating of the device located directly over the Gate pin



FSMD-A MS

Mechanical samples of the FSMD-A package with no die

Disclaimers

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Patents

Freebird Semiconductor holds numerous U.S patent. Any that apply to the product(s) listed in this document are identified by markings on the product(s) or on internal components of the product(s) in accordance with U.S Patent laws

eGaN is a registered trademark of Efficient Power Conversion Corporation, Inc. Data and specification subject to change without notice.

Revision

Datasheet Revision	Product Status
REV P	Proposal/development
REV Q	Characterization and Qualification
REV 1.0	Production Released

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Freebird Semiconductor Corporation has been assessed and registered by Intertek as conforming to the requirements of AS9100C and ISO 9001:2008 quality management system which is applicable to the design and manufacture of high reliability semiconductors devices and circuits. The assessment was performed in accordance with the requirements of AS9104/1:2012-01. Intertek is accredited under the Aerospace Management Program and IAQG ICOP scheme.